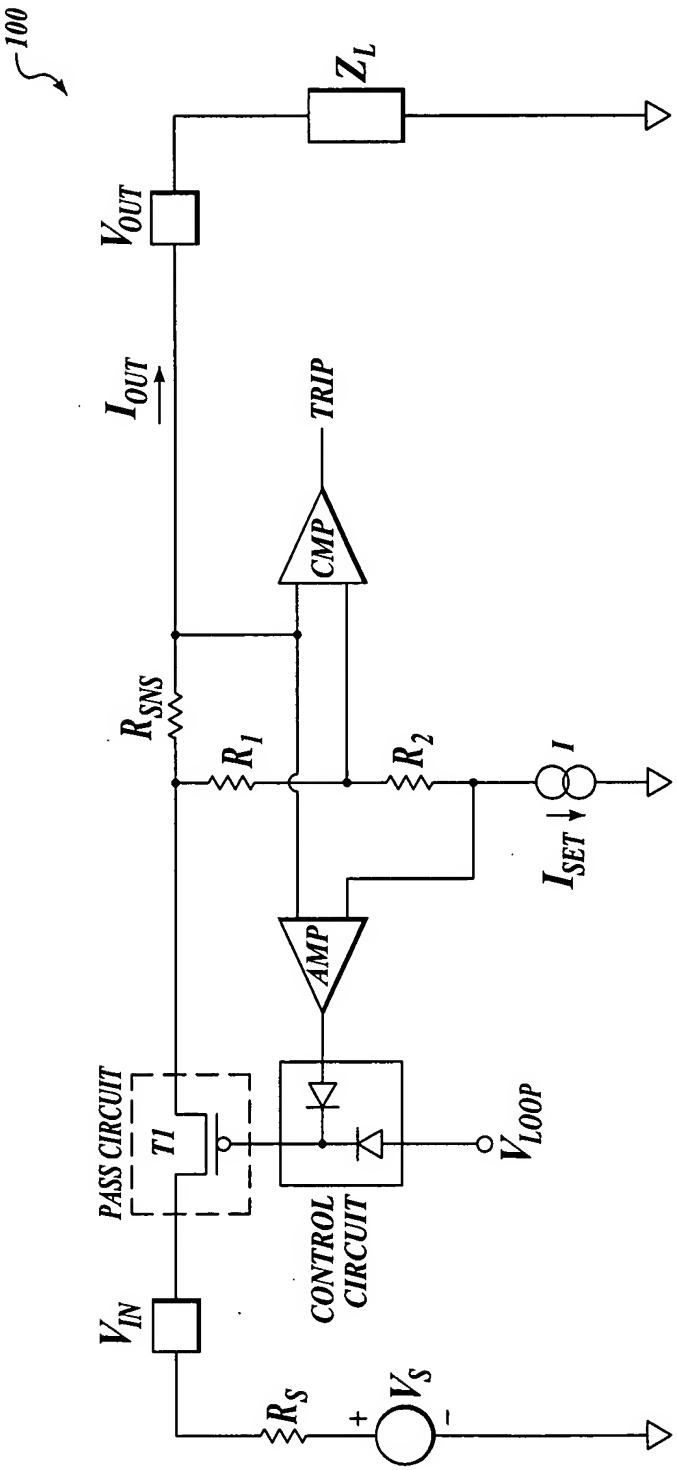

FIGURE 1

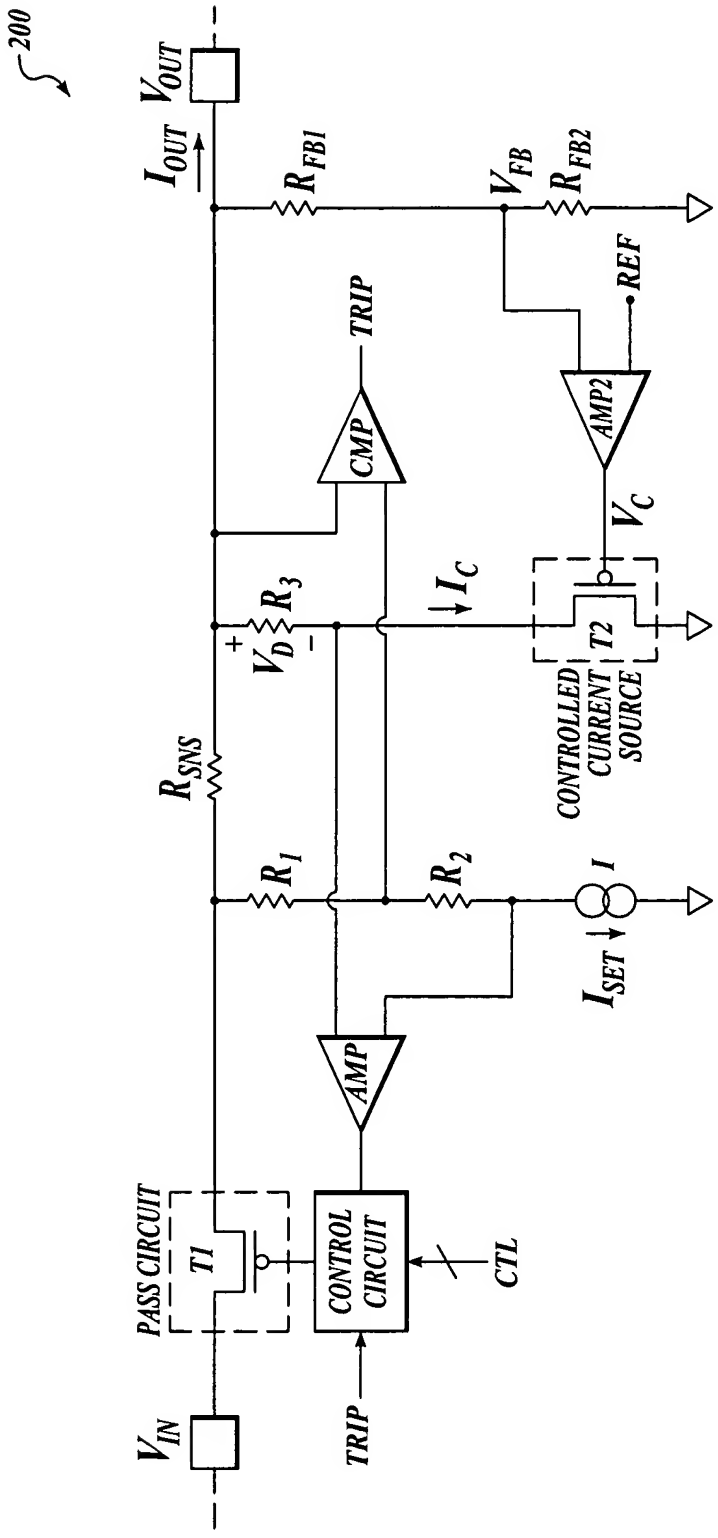
200
V_{OUT}
I_{OUT}
R_{FB1}
V_{FB}
R_{FB2}
REF
TRIP
CMP
AMP2
V_C
I_C
R_3
V_D
T2
CONTROLLED CURRENT SOURCE
R_{SNS}
R_1
R_2
I
I_{SET}
AMP
PASS CIRCUIT
T1
CONTROL CIRCUIT
CTL
V_{IN}
TRIP

FIGURE 2

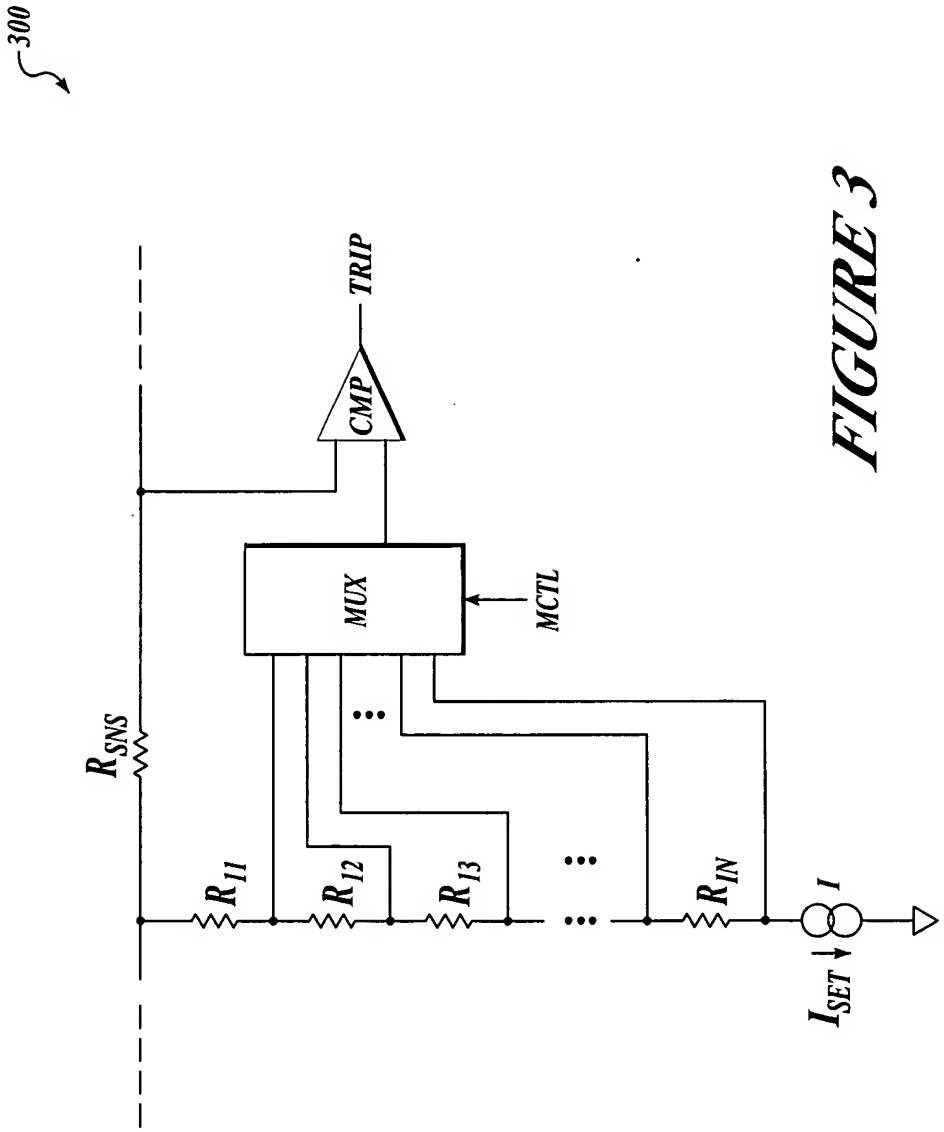

FIGURE 3

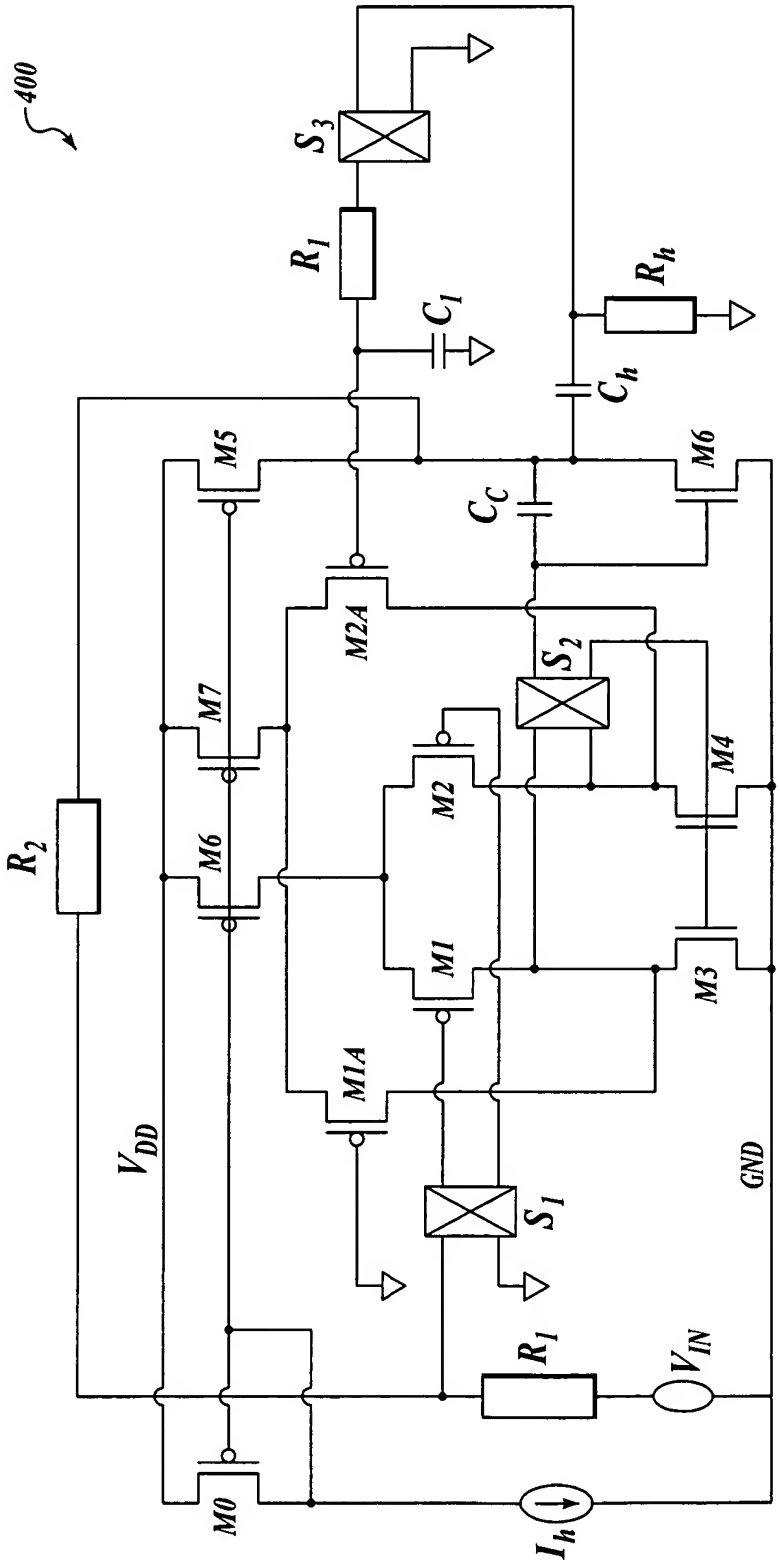
400
S_3
R_1
C_1
R_h
C_h
M5
M6
C_C
M2A
S_2
M7
M4
M2
R_2
M6
M1
M3
M1A
V_{DD}
GND
S_1
R_1
V_{IN}
M0
I_h

FIGURE 4

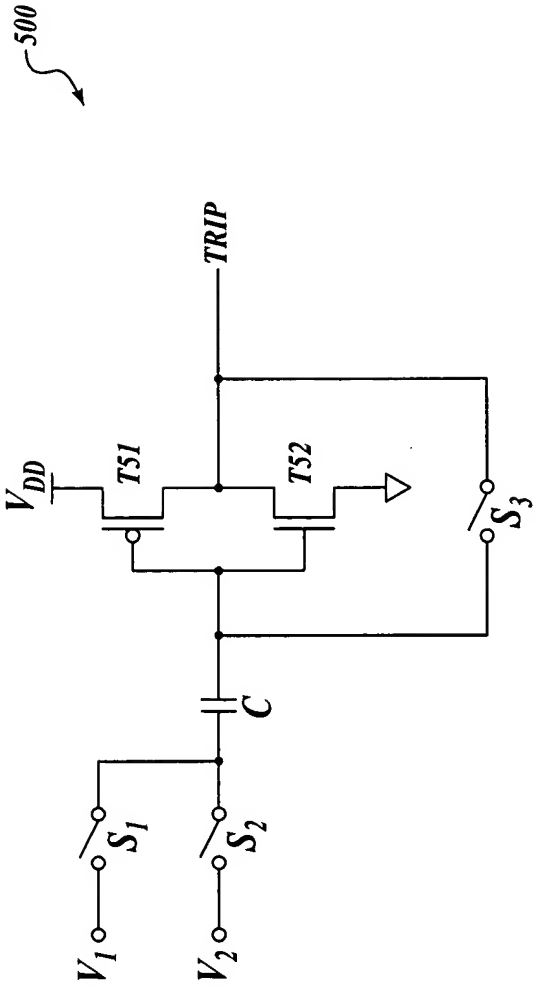

FIGURE 5

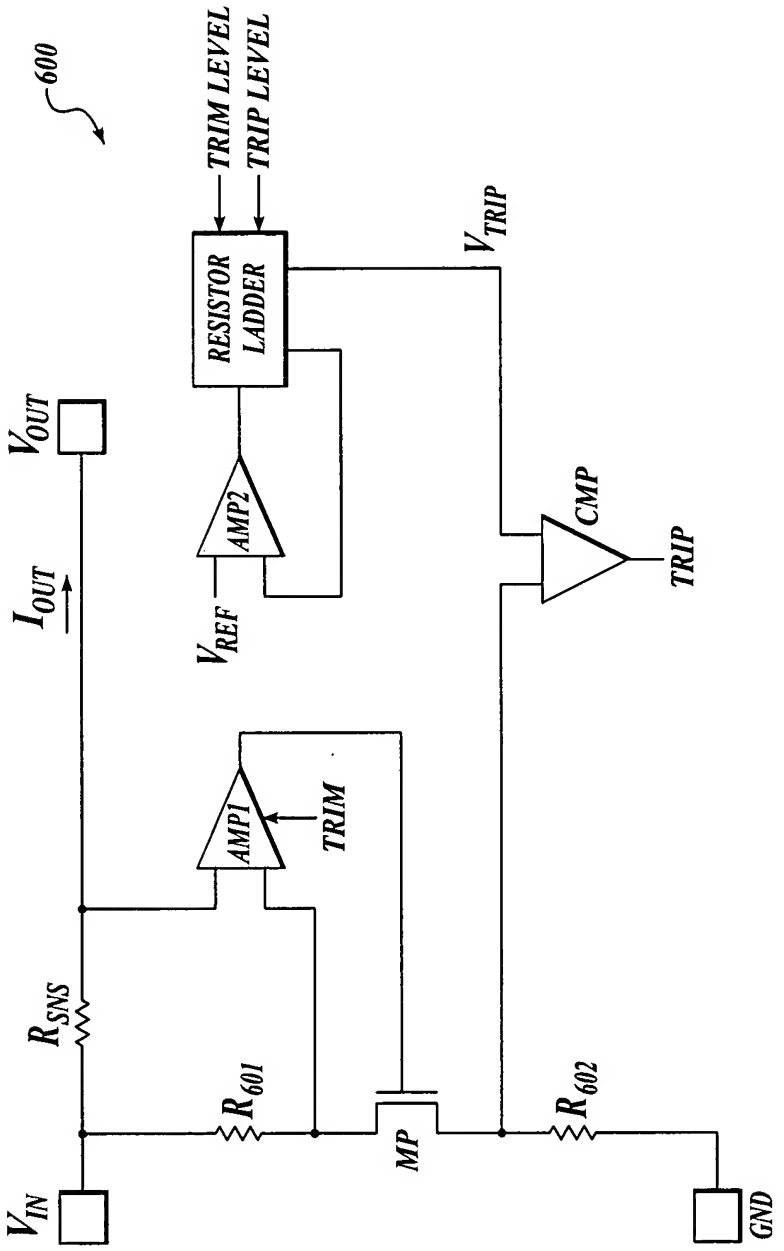

FIGURE 6 *(PRIOR ART)*